

Amendments to the Specification:

Please replace the paragraph beginning at page 5, line 22 with the following amended paragraph:

FIG. 1 illustrates a full hardware configuration of a multi-processor chip 10 configured according to the present invention. Ultimately, a multi-processor chip under development will have processors 1 through N, including processor 1 (12) and processors 2 through N (14), in hardware on a single silicon chip. According to the present invention, one of these processors 12 can be a previously designed, production processor, such as, the StrongARM™ brand processor available from Intel, Inc. The other processors 14 can be newly designed processors that are part of the hardware on the single silicon chip. In an embodiment, the multi-processor chip is designed for internetworking applications, but a skilled artisan will recognize that other applications may employed according to the invention.

Please replace the paragraph beginning at page 6, line 16 with the following amended paragraph:

Applications software 1 through N, including applications software 1 (20) and applications software 2 through N (24), can be applications software that execute on processors 1 through N, respectively. The first applications software 20 executes on the first processor 12 and has procedures that communicate with other portions of the multi-chip processor. Furthermore, the first applications software 20 interfaces to the rest of the chip through a hardware abstraction layer (HAL) 22, which is a set of function calls.

Please replace the paragraph beginning at page 7, line 1 with the following amended paragraph:

Applications software 2 through N (24) run on processors 2 through N (14), respectively. In an embodiment, applications software 2 through N can be programs to perform processing of packets or frames in Frame Relay and Ethernet applications.